	L#	Hits	Search Text	DBs
1	L1	885	257/686.ccls.	USP AT; US-P GPU B
2	L2	218	1 and connector\$1	USP AT; US-P GPU B
3	L3	3	("5579207" "6093209" "6278284").PN.	USP AT
4	L4	9	("4647123" "5164916"	USP
5	L5	254	361/790.ccls.	USP AT; US-P GPU B
6	L6	224	5 not 2	USP AT; US-P GPU B
7	L7	95	1 and socket\$1	USP AT; US-P GPU B
8	L8	56	7 not (2 6)	USP AT; US-P GPU B

	L#	Hits	Search Text	DBs
9	L9	480	361/803.ccls.	USP AT; US-P GPU B
10	L10	392	9 not (2 6 8)	USP AT; US-P GPU B
11	L11	230	10 and (connector\$1 socket\$1)	USP AT; US-P GPU B
12	L12	201	439/69.ccls.	USP AT; US-P GPU B
13	L13	173	12 not (2 6 8 11)	USP AT; US-P GPU B
14	L14	587	439/74.ccls.	USP AT; US-P GPU B
15	L15	510	14 not (2 6 8 11 13)	USP AT; US-P GPU B

	L#	Hits	Search Text	DBs
16	L16	425	15 and (connector\$1 socket\$1)	USP AT; US-P GPU B
17	L17	365	1 and pin\$1	USP AT; US-P GPU B
18	L18	196	17 not (2 6 8 11 13)	USP AT; US-P GPU B
19	L19	196	17 not (2 6 8 11 13 16)	USP AT; US-P GPU B
20	L20	286	257/685.ccls.	USP AT; US-P GPU B
21	L21	165	20 not (2 6 8 11 13 16 19)	USP AT; US-P GPU B
22	L23	366	438/109.ccls.	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
23	L24	308	23 not (2 6 8 11 13 16 19 21)	USP AT; US-P GPU B
24	L25	493	361/785.ccls.	USP AT; US-P GPU B
25	L26	385	25 not (2 6 8 11 13 16 19 21 24)	USP AT; US-P GPU B
26	L27	728	257/777.ccls.	USP AT; US-P GPU B
27	L28	493	27 not (2 6 8 11 13 16 19 21 24 26)	USP AT; US-P GPU B
28	L29	89	28 and (connector\$1 socket\$1)	USP AT; US-P GPU B
29	L34	543	(25 27) and pin\$1	USP AT; US-P GPU B

	L#	Hits	Search Text	DBs
30	L35	79	34 not (2 6 8 11 13 16 19 21 24 26 29)	USP AT; US-P GPU B
31	L36	275	15 and pin\$1	USP AT; US-P GPU B
32	L37	11	36 not (2 6 8 11 13 16 19 21 24 26 29 35)	USP AT; US-P GPU B
33	L38	1102	257/723.ccls.	USP AT; US-P GPU B
34	L39	449	38 and stack\$4	USP AT; US-P GPU B
35	L40	247	39 not (2 6 8 11 13 16 19 21 24 26 29 35 37)	USP AT; US-P GPU B
36	L41	610	257/691.ccls.	USP AT; US-P GPU B

	L#	Hits	Search Text	DBs
37	L42	172	41 and module\$1	USP AT; US-P GPU B
38	L43	254	((38 not 39) (41 not 42)) and (connector\$1 socket\$1)	USP AT; US-P GPU B
39	L44	198	43 not (2 6 8 11 13 16 19 21 24 26 29 35 37 40)	USP AT; US-P GPU B